AMENDMENTS TO THE SPECIFICATION

Page 1, second paragraph, delete in its entirety, and replace with the following:

The invention is based oon a priority application DE 100 36 827.1 which is hereby incorporated by reference.

Page 5, first full paragraph, delete in its entirety, and replace with the following:

The clock-pulse supply unit additionally comprises a changeover switch C and an artificial line B. The changeover switch C has one input and two outputs. The input is connected to the output of the transmitter unit E. One of the outputs of the changeover switch C is connected to the artificial line B, the other to the bus for system clock-pulse distribution. The changeover switch C has a control input through which the input of the changeover switch C can be optionally connected to one or other output either of the outputs of the changeover switch C. Prior to the changeover from the slave clock pulse to the redundant clock pulse, the input is connected to the artificial line B. The artificial line B is constructed, for example, of passive components and serves to simulate a real line of a bus for system clock-pulse distribution, with the same electrical parameters. In the simplest case, for example, the artificial line B is a 100 ohm resistance or an RLC element. Real conditions, including load, can thus be simulated by connecting the redundant clock pulse to the artificial line B via the transmitter unit E. The phase comparison in the phase detector can thus be effected under approximately real operating conditions. Following completion of synchronization, the switchover to the redundant clock pulse is effected by feeding the redundant clock pulse into the bus for system clock-pulse distribution A of the back panel via the changeover